IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.	:	10/518,974
Confirmation No.	:	2918
Applicant	:	Richard Michael Taylor
Title (as amended):		Microprocessor Instruction Execution Method for Exploiting Parallelism by Time Ordering Operations in a Single Thread at Compile Time"
Filed	:	December 21, 2004
Art Unit	:	2183
Examiner	:	Huisman, David J.

Attorney Docket No.   356884.00002

FILED ELECTRONICALLY ON April 16, 2009

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY AND AMENDMENT UNDER 37 CFR §1.116
TO EXAMINER'S FINAL ACTION MAILED FEBRUARY 24, 2009

Sir:

This is in response to the final Office Action, mailed February 24, 2009, having a period for response set to expire on May 24, 2009. The following amendments and remarks are respectfully submitted.

Amendments to the claims begin on page 2 of this paper; Remarks begin on page 7 of this paper.